PATENT COOPERATION TREATY

PCT

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

(Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference			FOR FURTHER A	CTION	See Form PCT/IPEA/416				
P35209-P0									
International application No.				International filing date (day/month/year) Priority date (day/month/year)					
PCT/JP2004/011427			03.08.200		06.08.2003				
International Patent Classification (IPC) or national classification and IPC									
G06F12/06, 3/06, 3/08, 12/00, G06K17/00, 19/07									
Applicant MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.									
1. This report is the international preliminary examination report, established by this International Preliminary Examining Authority under Article 35 and transmitted to the applicant according to Article 36.									
2. This	REPORT consists of	of a total of	8	sheets, including	g this cover sheet.				
3. This	report is also accom	panied by Al	NNEXES, comprising:						
a.	(sent to the a	pplicant and	to the International Bi	ureau) a total of 9	sheets, as follows:				
sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications authorized by this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions).									
sheets which supersede earlier sheets, but which this Authority considers contain an amendment that goes beyond the disclosure in the international application as filed, as indicated in item 4 of Box No. I and the Supplemental									
	Box.								
b.	(sent to the l	nternational l	Bureau only) a total of	(indicate type and number	r of electronic carrier(s))				
					, containing a sequence listing and/or tables				
related thereto, in computer readable form only, as indicated in the Supplemental Box Relating to Sequence Listing (see Section 802 of the Administrative Instructions).									
4. This	report contains indi	cations relation	ng to the following iter	ns:					
	Box No. I	Basis of the	report						
	Box No. II	Priority							
	Box No. III	Non-establis	shment of opinion with	regard to novelty, inventi	ive step and industrial applicability				
	Box No. IV	Lack of unit	y of invention						
	Box No. V		atement under Article lexplanations supporti		ity, inventive step or industrial applicability;				
	Box No. VI	Certain docu	uments cited						
	Box No. VII	Certain defe	cts in the international	application					
	Box No. VIII Certain observations on the international application								
Date of submission of the demand Date of completion of this report									
				-					
Name and mailing address of the IPEA/JP				Authorized officer					
İ									
Facsimile No.				Telephone No.					

Translation

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International application No.

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Box	x No. I	Basis of the report	
1.	With regar	rd to the language, this report is based on the internation under this item.	nal application in the language in which it was filed, unless otherwise
		s report is based on translations from the original language of is the language of a translation furnished for the purpointernational search (Rule 12.3 and 23.1(b)) publication of the international application (Rule 12.4) international preliminary examination (Rule 55.2 and/	oses of:
2.	receiving this report	Office in response to an invitation under Article 14 are	report is based on (replacement sheets which have been furnished to the ereferred to in this report as "originally filed" and are not annexed to
	the o	description:	
	page	es <u>1-48</u>	as originally filed/furnished
	page	es*	received by this Authority on
	page	es*	received by this Authority on
	the o	claims:	
	nos.	3,5-15,17-21,23-27	as originally filed/furnished
	nos.	*	as amended (together with any statement) under Article 19
	nos.	* 1,2,4,16,22	received by this Authority on 03.06.2005
	nos.		
	M the	drawings:	
	shee	-	as originally filed/furnished
	shee		
	shee		
		quence listing and/or any related table(s) – see Supplems	ental Box Relating to Sequence Listing.
3.	L_ The	amendments have resulted in the cancellation of:	
	님	the description, pages	
	닐	the claims, nos.	
		the drawings, sheets/figs	
		the sequence listing (specify):	
	Ш	any table(s) related to sequence listing (specify):	
4.	This they	s report has been established as if (some of) the amenda have been considered to go beyond the disclosure as file	ments annexed to this report and listed below had not been made, since ed, as indicated in the Supplemental Box (Rule 70.2(c)).
		the description, pages	
		the claims, nos.	
		the sequence listing (specify):	
		any table(s) related to sequence listing (specify):	
*	If item 4 a	pplies, some or all of those sheets may be marked "supe	

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		ent under Article 35(2) with regard to novelty, inventive step or industrial applicability; lanations supporting such statement		
1.	Statement			
	Novelty (N)	Claims	1-27	YES
		Claims		NO
	Inventive step (IS)	Claims	7, 8, 10-13, 15-27	YES
		Claims	1-6, 9, 14	NO
	Industrial applicability (IA)	Claims	1-27	YES
		Claims		NO

2. Citations and explanations (Rule 70.7)

Document 1: JP 6-350907 A (Fuji Photo Film Co., Ltd.), 22 December 1994

Document 2: JP 2001-36904 A (Fuji Photo Film Co., Ltd.), 09 February 2001

Document 3: JP 7-28968 A (Intel Corp.), 31 January 1995

Document 4: JP 2001-184257 A (Sony Computer Entertainment Inc.), 06 July 2001

Document 5: JP 2000-122923 A (Sony Corp.), 28 April 2000

Claims 1 to 6, 9 and 14

The inventions set forth in claims 1 to 6, 9 and 14 do not involve an inventive step in the light of documents 1 to 5.

Document 1 discloses a feature wherein information pertaining to the storing speed, the type of storage element, the storage capacity and the like is maintained in the card attribute information area of the flash memory card so that it is possible to output said information to an external device.

Document 2 discloses a feature wherein the card information structure (CIS) that is stored in a memory card is accessed in order to determine the recording speed with which it is possible to record to said memory

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Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

card (refer to paragraph [0042]), and further discloses a feature whereby a host device queries an external device as to whether a prescribed transfer speed is supported and then establishes the transfer speed for both devices when the external device responds that a given rotational speed is "supported" (fig. 3).

Document 3 discloses a feature wherein information pertaining to the access speed, the access period and the like for each power source voltage is maintained in the flash memory card so that it is possible to output said information to an external device. Therein, it is possible to know the configuration range of the power source voltages for the flash memory card (refer to paragraph [0050]), and thus the configuration in question can be considered to maintain power source voltage value information as well.

Document 4 discloses a feature whereby information pertaining to the page size is maintained in the flash memory card so that it is possible to output said information to an external device.

Document 5 discloses a feature whereby information that is necessary for deriving the interleave factor when writing data in parallel (e.g. information pertaining to the data input period and the program period) is maintained within the flash memory card so that it is possible to output said information to an external device (refer to paragraph [0244]).

In the light of the items that are disclosed in documents 1 to 5, it would have been easy for a person skilled in the art to conceive of maintaining the various types of information that are set forth in claims 1 to 6, 9 and 14 within a memory card, and to conceive of a

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Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

format wherein various queries are transmitted from the host device to the memory card.

Furthermore, in the invention that is disclosed in document 3 the power source voltage can be said to be a condition for accessing the flash memory card, and thus is considered to correspond to the "access conditions" in the invention that is set forth in claim 1. Likewise, the access speed, the access period and the like are considered to correspond to the "access performance" in the invention that is set forth in claim 1. As a result, document 3 can be considered to disclose the feature of storing information pertaining to the access conditions and the access performance under said access conditions.

Claims 7 and 8

The invention set forth in claims 7 and 8 is novel and involves an inventive step.

The features that are set forth in claims 7 and 8 are not disclosed in any of the documents, and would not have been obvious to a person skilled in the art.

Claims 10 to 13

The invention set forth in claims 10 to 13 is novel and involves an inventive step.

The feature of storing information pertaining to the access speed at a plurality of power consumption levels in addition to the information pertaining to the access conditions is not disclosed in any of the documents, and would not have been obvious to a person skilled in the art.

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Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

Claim 15

The invention set forth in claim 15 is novel and involves an inventive step.

The feature wherein the processing unit size, the access method and the processing content are included among the access conditions is not disclosed in any of the documents, and would not have been obvious to a person skilled in the art.

Claims 16 to 27

The inventions set forth in claims 16 to 27 are novel and involve an inventive step.

The feature of determining the access conditions from the information pertaining to the access conditions and the access performance under said access conditions which is acquired from the semiconductor memory card, the information pertaining to the access conditions that can be utilized by the access device, and the information pertaining to the access speed that is required of the semiconductor memory card, and then controlling so that the files are accessed in accordance with said access conditions is not disclosed in any of the documents that are cited in the international search report, and would not have been obvious to a person skilled in the art.

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Box No. VIII Certain observations on the international application

The following observations on the clarity of the claims, description, and drawings or on the question whether the claims are fully supported by the description, are made:

1.

Claim 1 sets forth a feature wherein both information pertaining to the access conditions and information pertaining to the access performance are stored in the card information storage unit. On the other hand, claim 2 includes the disclosure "at least one set of information...is stored," which indicates that it is acceptable for only one of these two sets of information to be stored. As a result, the disclosures in question contradict one another.

2.

In the section of the disclosure that pertains to the card information acquisition section, claim 16 indicates that the "information that was acquired from the aforementioned card information acquisition section" of the access condition determination unit includes not only information pertaining to the access conditions but also information pertaining to the access performance that is exhibited by the semiconductor memory card. Consequently, the inclusion of the disclosure "information pertaining to the access performance of the semiconductor memory card" in addition to the disclosure "information that was acquired from the aforementioned card information acquisition section" within the section of the disclosure that pertains to the card information acquisition section is redundant and only serves to obscure exactly what information is used in order to determine the access conditions.

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(Furthermore, the access condition determination step that is set forth in claim 22 does not set forth the information pertaining to the access performance.)

3.

With regards to the disclosure "when the aforementioned access device accesses the aforementioned semiconductor memory card" in claim 22, the access device is not indicated prior thereto in any of the disclosures that precede the disclosure in question.